

**Marked up Version of the Claims**

5. (amended) Device according to [once of the preceding claims] Claim 4, characterized in that the second circuit (2) exhibits a second digital signal multiplexer (18) which is connected to the second winding of the transformer (3).

6. (amended) Device according to [once of the preceding claims] Claim 5, characterized in that the second circuit (2) exhibits an oscillator circuit (14) which provides the clock for the second digital signal multiplexer (18).

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